



PERI INSTITUTE OF TECHNOLOGY

Mannivakkam-48

Department of Electronics and Communication Engineering



Ref: PERIIT/ECE/SEMINAR/2022-2023

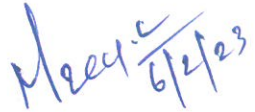
Date: 06.02.2023

CIRCULAR

This is to inform you that our Department is planning to conduct a Seminar on “**Digital Electronics**” (Online) for the academic Year 2022-23. All ECE students directed to attend the program.

S.NO	Year	Scheduled Date	Time
1	IV,III& II	06-02-2023	10.00 AM to 12.00 PM


Program Co-ordinator


HOD/ECE

Copy To:

1. Principal
2. Vice principal
3. IQAC
4. Faculty members ECE
5. Main – Notice board


Dr. R. PALSON KENNEDY, M.E., Ph.D.,
PRINCIPAL
PERI INSTITUTE OF TECHNOLOGY
Mannivakkam, Chennai - 600 048.

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9 FEB 2023
10 AM

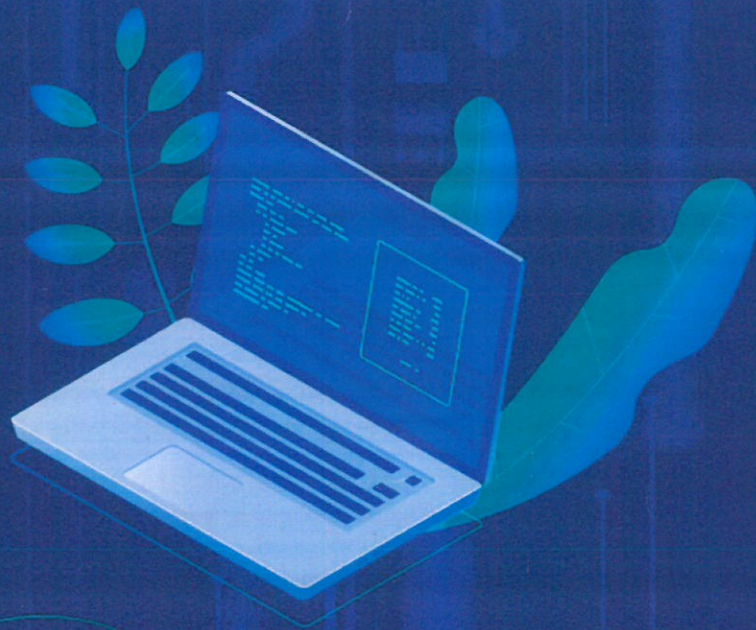
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ORGANIZES

INTERNATIONAL SEMINAR

ON

"DIGITAL ELECTRONICS"



Resource Person:

Mr. ERWIN SUTANTO

Department of Physics
Universitas Airlangga,
Indonesia

Dr. R. PALSON KENNEDY

Principal

Mr. B. MAGESH


Vice-Principal

Dr. M. RAMKUMAR PRABHU

HOD - ECE

Organizer:

ADMISSION CONTACT :

 **91505 94111 / 222**



www.peri.education

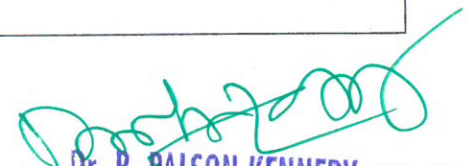
admissions@peri.education



PERI Knowledge Park, Mannivakkam, Chennai - 600048

REPORT ON EVENT

Department	Electronics and Communication Engineering
Type of Event	Seminar
Title of the Event	“DIGITAL ELECTRONICS”
Date and Time	09-02-2023 & 10.00 AM to 1.00 PM
Mode of Event	Google Meet (Online)
Details of Participants	For all ECE Faculties & students
Details of the Resource Person	Name of the Guest Speaker : Mr. ERWIN SUTANTO Organization : University Airlangga, Indonesia Designation : Lecturer, Department of Physics
Report on Event	The Aim of an event was to teach the Basics of Digital Electronics . In the first session of Seminar, the basics of Electronics, Electrical tools essential to build electronic circuits, safety measures to be followed, symbols used to illustrate electronic components and Introduction to CMOS circuits and gates was explained. Seminar was mainly focused on layout of CMOS circuits and logic gates. At the end of program participants were able to sketch the schematic layout of CMOS Gates.
Feedback of the Resource Person	Mr. ERWIN SUTANTO observed that the college has a good infrastructure and dedicated staff members. Further, the administrator and the Principal were very innovative and dynamic. So that they have organized the program in an efficient manner. The students are very attentive, creative and dynamic. I pray that the college will start research program in the functioning years.
Outcome of the Event	Enhanced the knowledge in the field of DIGITAL ELECTRONICS .


Dr. R. PALSON KENNEDY, M.E., Ph.D
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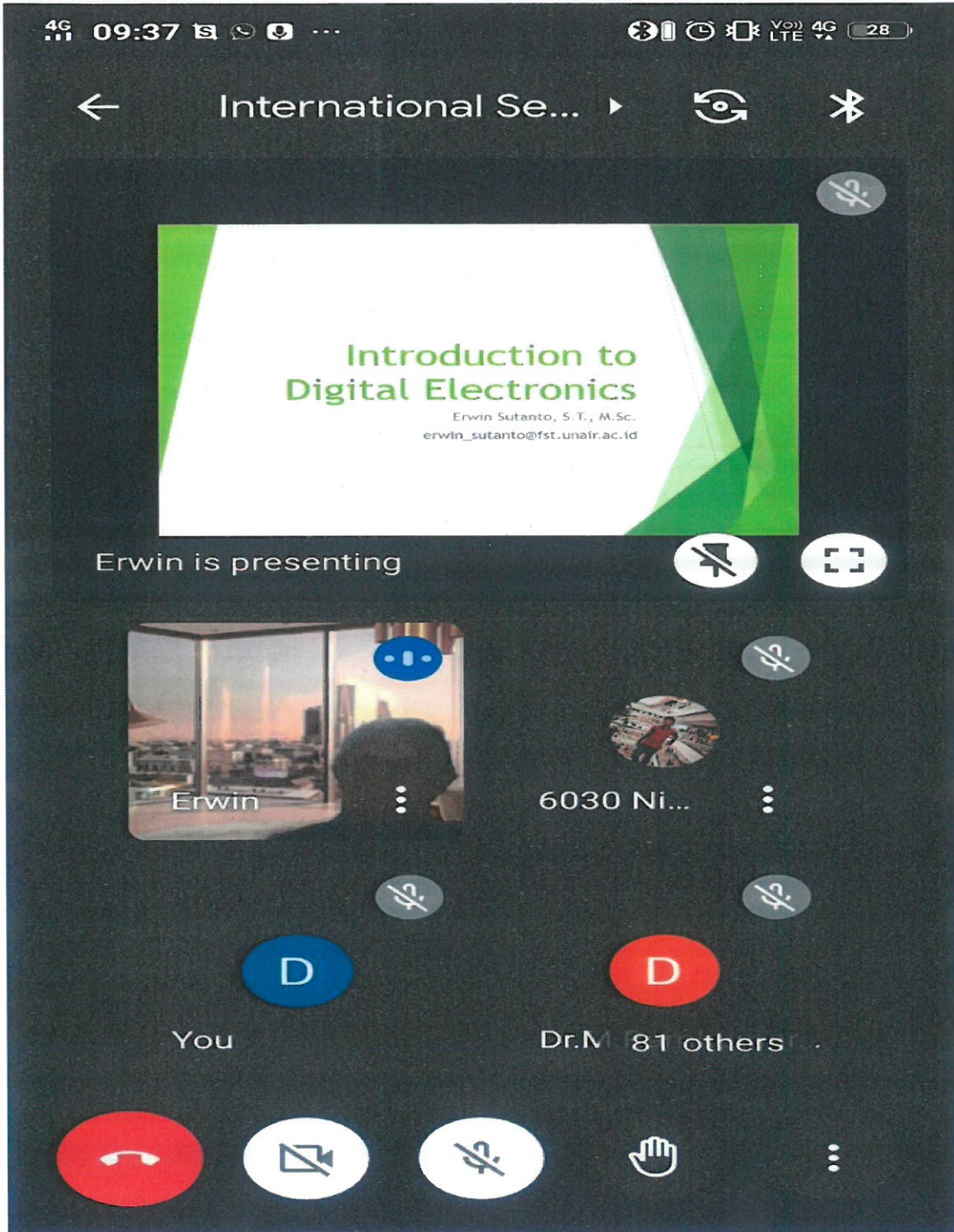


Figure 1: Mr. ERWIN SUTANTO presented on the topic of Digital Electronics

R. Palson Kennedy
Dr. R. PALSON KENNEDY, M.E., Ph.D.,
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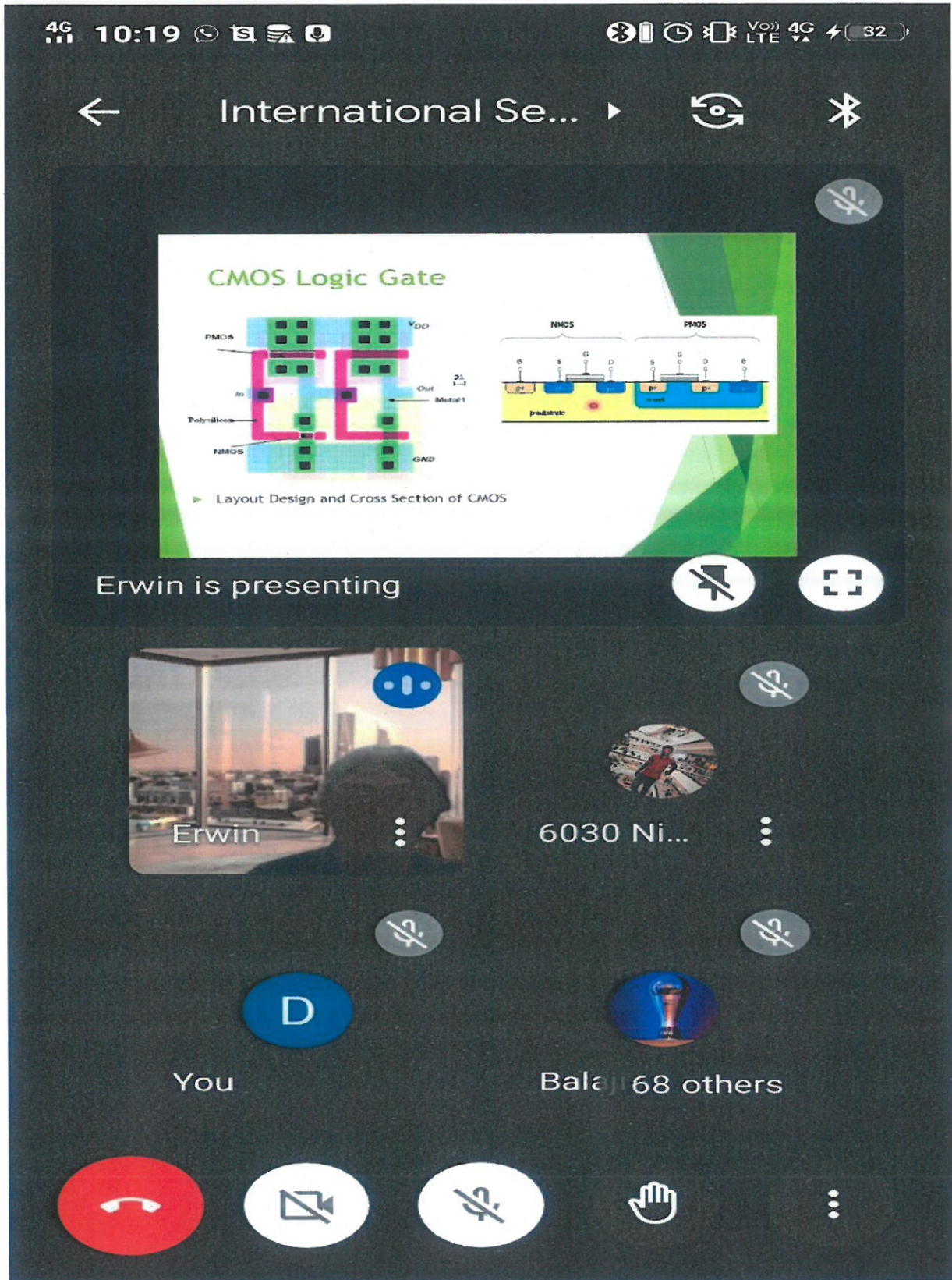


Figure 2: Presentation on CMOS Logic Gates

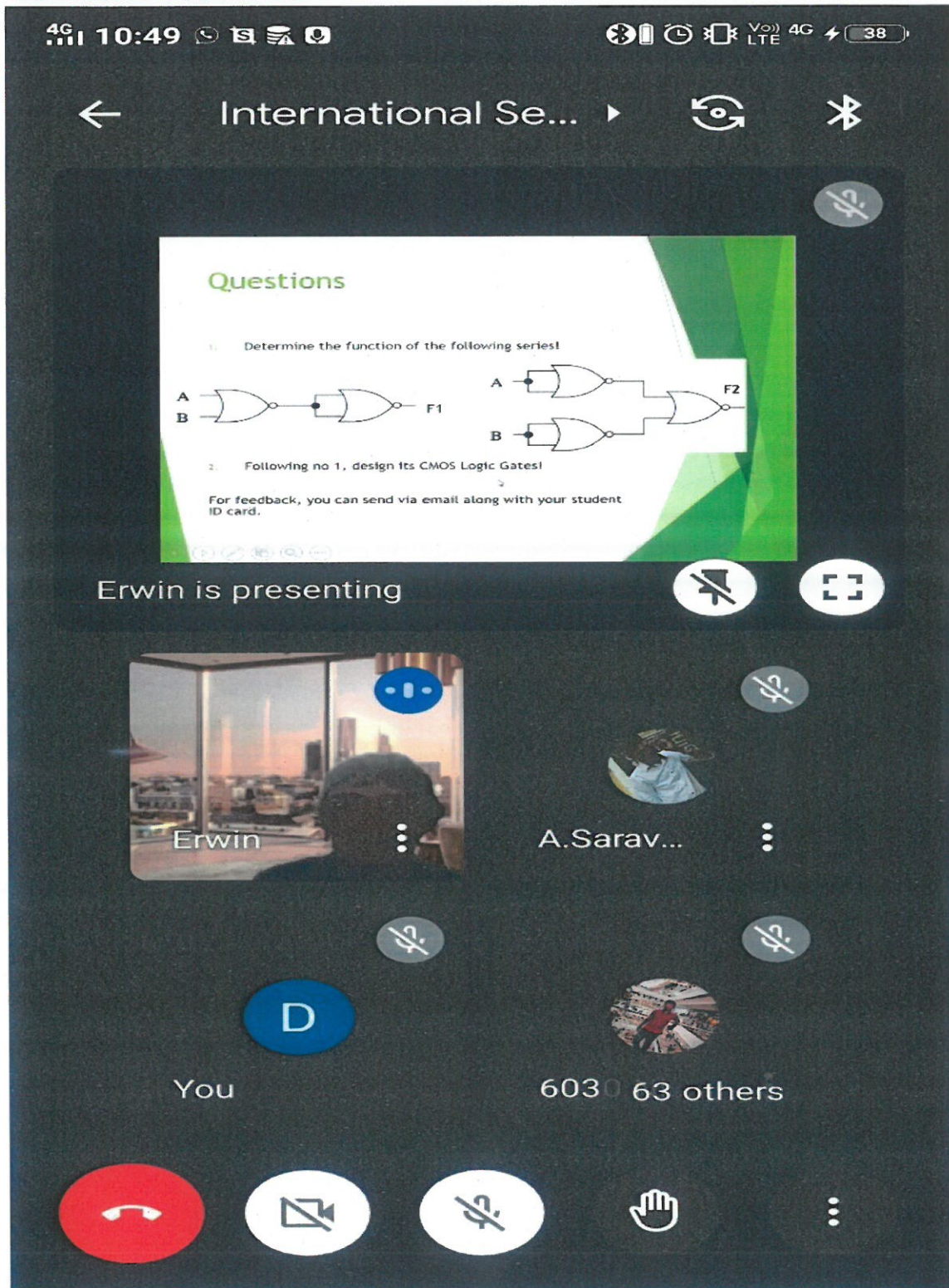


Figure 3: Discussion and Doubt Clarification session

Dr. R. Palson Kennedy
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OF PARTICIPATION

This is to certify that Mr / Ms. MONISHA.E
from II YEAR - ECE has participated
in International Seminar on **DIGITAL ELECTRONICS** held at **PERI Institute of
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This is to certify that Mr / Ms. ANEES FATHIMA .M
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in International Seminar on **DIGITAL ELECTRONICS** held at **PERI Institute of
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from III YEAR - ECE has participated
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This is to certify that Mr/ Ms. SANGEETHA.V
from IV YEAR - ECE has participated
in International Seminar on **DIGITAL ELECTRONICS** held at **PERI Institute of
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